

**REPLY UNDER 37 C.F.R. § 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2173**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 10/643,810

Filed: August 19, 2003

Inventor(s):  
King, et al.

Title: COMPUTER ASSEMBLY

Examiner: Vu, Kieu D.  
Group/Art Unit: 2173  
Atty. Dkt. No: 5681-70200  
Confirmation No.: 4786

\*\*\*\*\*CERTIFICATE OF E-FILED TRANSMISSION\*\*\*\*\*

I hereby certify that this correspondence is being transmitted via electronic filing to the United States Patent and Trademark Office on the date shown below

Rory D. Rankin

Printed Name

/ Rory D. Rankin /

Signature

August 27, 2007

Date

**RESPONSE TO FINAL OFFICE ACTION MAILED**

**JUNE 26, 2007**

**Mail Stop AF**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

This paper is submitted in response to the Final Office Action of June 26, 2007, to further highlight why the application is in condition for allowance. Please amend the case as listed below.